	·		T	
-	7	audio and audio with record\$4 and input	USPAT;	2004/03/17
		with microphone and first with input\$4	US-PGPUB;	14:26
		and second with input\$4 and compress\$4	EPO; JPO;	
		and de\$compress\$4 and conver\$5 with	IBM_TDB	
		analog with digital and conver\$5 with		
		"a/d" and conver\$5 with "d/a" and		
		switch\$4 and switch\$4 with digital\$4 and		
		switch\$4 with analog not sony.as.		
-	7	audio and audio with record\$4 and input	USPAT;	2004/03/17
		with (microphone mike) and first with	US-PGPUB;	15:01
		input\$4 and second with input\$4 and	EPO; JPO;	
		compress\$4 and de\$compress\$4 and conver\$5	IBM TDB	
		with analog with digital and conver\$5	_	
		with "a/d" and conver\$5 with "d/a" and		
		switch\$4 and switch\$4 with digital\$4 and		
	İ	switch\$4 with analog not sony.as.		
<u> </u>	0	first adj compress\$4 and second adj	USPAT;	2004/03/17
		compress\$4 and memory adj (device card	US-PGPUB;	15:01
		unit) and first adj memory and second adj	EPO; JPO;	
		memory and signal and controller and	IBM TDB	
		(determin\$4 identif\$4) with memory and	15.1_155	
		record\$4 and audio and input\$4 with audio		
1_ `	1	first adj compress\$4 and second adj	USPAT;	2004/03/17
	*	compress\$4 and memory adj (device card	US-PGPUB;	15:01
		unit) and first adj memory and second adj	EPO; JPO;	10.01
		memory and signal and controller and	IBM_TDB	
		(determin\$4 identif\$4) with memory and		
	i ,	record\$4 and audio and input\$4 same audio	HCDAM.	2004/02/17
-	/	audio and audio with record\$4 and input	USPAT;	2004/03/17
		with (microphone mike) and first with	US-PGPUB;	15:02
	1	input\$4 and second with input\$4 and	EPO; JPO;	
		compress\$4 and de\$compress\$4 and conver\$5	IBM_TDB	
		with analog with digital and conver\$5		
		with "a/d" and conver\$5 with "d/a" and		
		switch\$4 and switch\$4 with digital\$4 and		
		switch\$4 with analog not sony.as. and		•
		input\$4 with audio		
<del>-</del>	81	copy\$right with compl\$6 and 713/\$.ccls.	USPAT;	2004/03/17
'			US-PGPUB;	16:20
			EPO; JPO;	
			IBM_TDB	
_	55840	(memory chip smart micro\$process\$4) near	USPAT;	2004/11/10
		card	US-PGPUB;	10:24
			EPO; JPO;	
			IBM_TDB	
-	52804	(memory chip smart micro\$process\$4) adj	USPAT;	2004/11/10
		card	US-PGPUB;	10:24
			EPO; JPO;	
			IBM TDB	
-	6201	(memory chip smart micro\$process\$4) near	USPAT;	2004/11/10
		card and signal with process\$4 with	US-PGPUB;	10:25
		circuit\$4	EPO; JPO;	
			IBM TDB	
-	4	(memory chip smart micro\$process\$4) near	USPAT;	2004/11/10
	1	card and signal with process\$4 with	US-PGPUB;	10:25
		circuit\$4 with copy\$right\$4	EPO; JPO;	
		orrested a when cobletifueds	IBM TDB	
I _	3	   (memory chip smart micro\$process\$4) near	USPAT;	2004/11/10
	1	card and signal with process\$4 with	US-PGPUB;	10:26
		circuit\$4 with copy\$right\$4 with	EPO; JPO;	10.20
_	220	protect\$4	IBM_TDB	2004/11/10
-	230	(memory chip smart micro\$process\$4) near	USPAT;	1
1	!	card and signal with process\$4 with	US-PGPUB;	10:28
l		circuit\$4 and copy\$right\$4 with protect\$4	EPO; JPO;	
!	_ :	1,	IBM_TDB	2224/11/22
-	3	(memory chip smart microsprocess\$4) near	USPAT;	2004/11/10
		card and (signal with process\$4 with	US-PGPUB;	10:27
		circuit\$4) same copy\$right\$4 with	EPO; JPO;	
1	1	protect\$4	IBM TDB	

-	21	(memory chip smart micro\$process\$4) near	USPAT;	2004/11/10
		card and signal with process\$4 with	US-PGPUB;	10:29
		circuit\$4 and copy\$right\$4 with protect\$4	EPO; JPO;	
		with signal\$4	IBM_TDB	
-	17	(memory chip smart micro\$process\$4) near	USPAT;	2004/11/10
		card and signal with process\$4 with	US-PGPUB;	10:30
		circuit\$4 and copy\$right\$4 with protect\$4	EPO; JPO;	
	<u>.</u>	with signal\$4 not sony.as. not utsumi.in.	IBM_TDB	